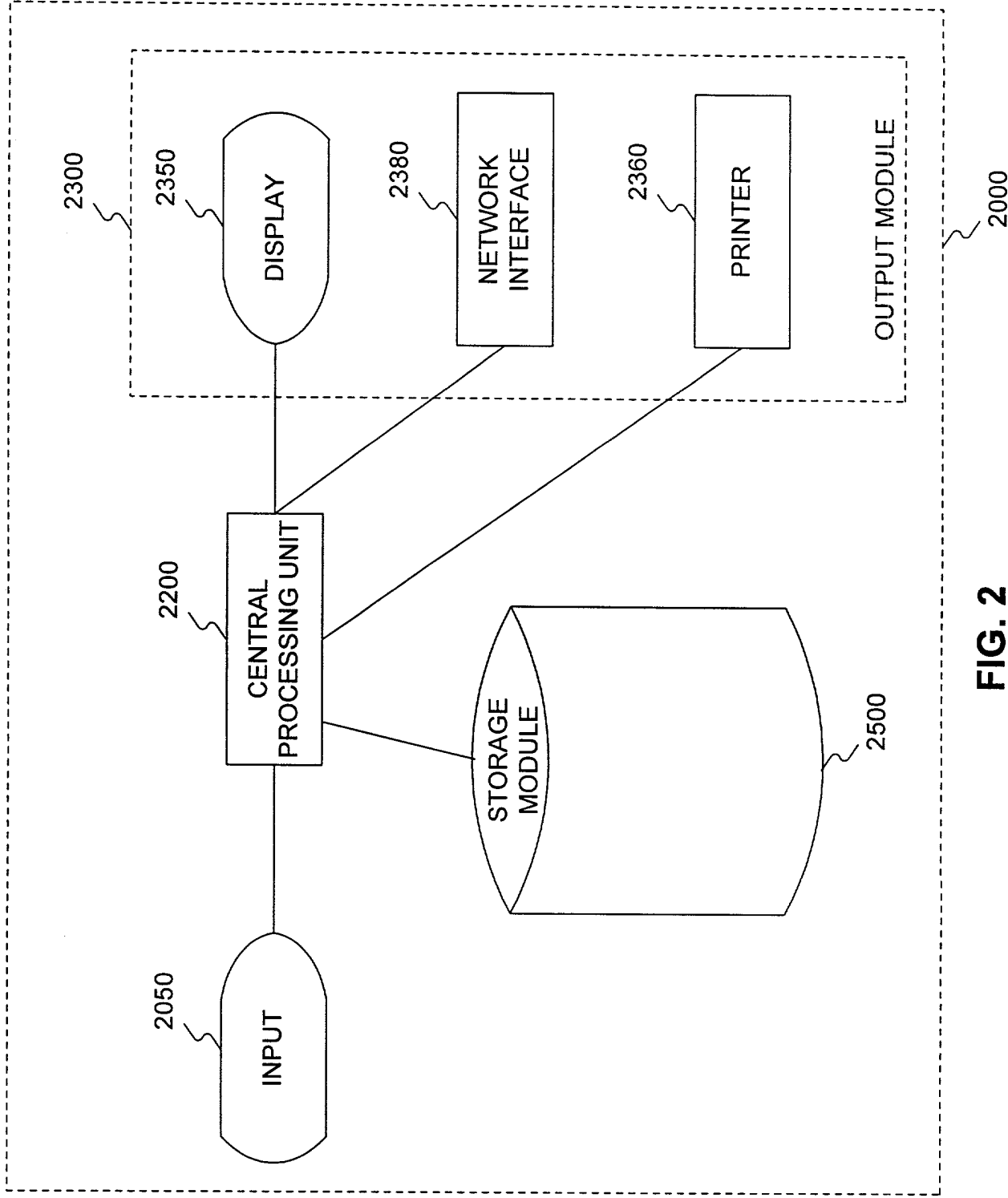
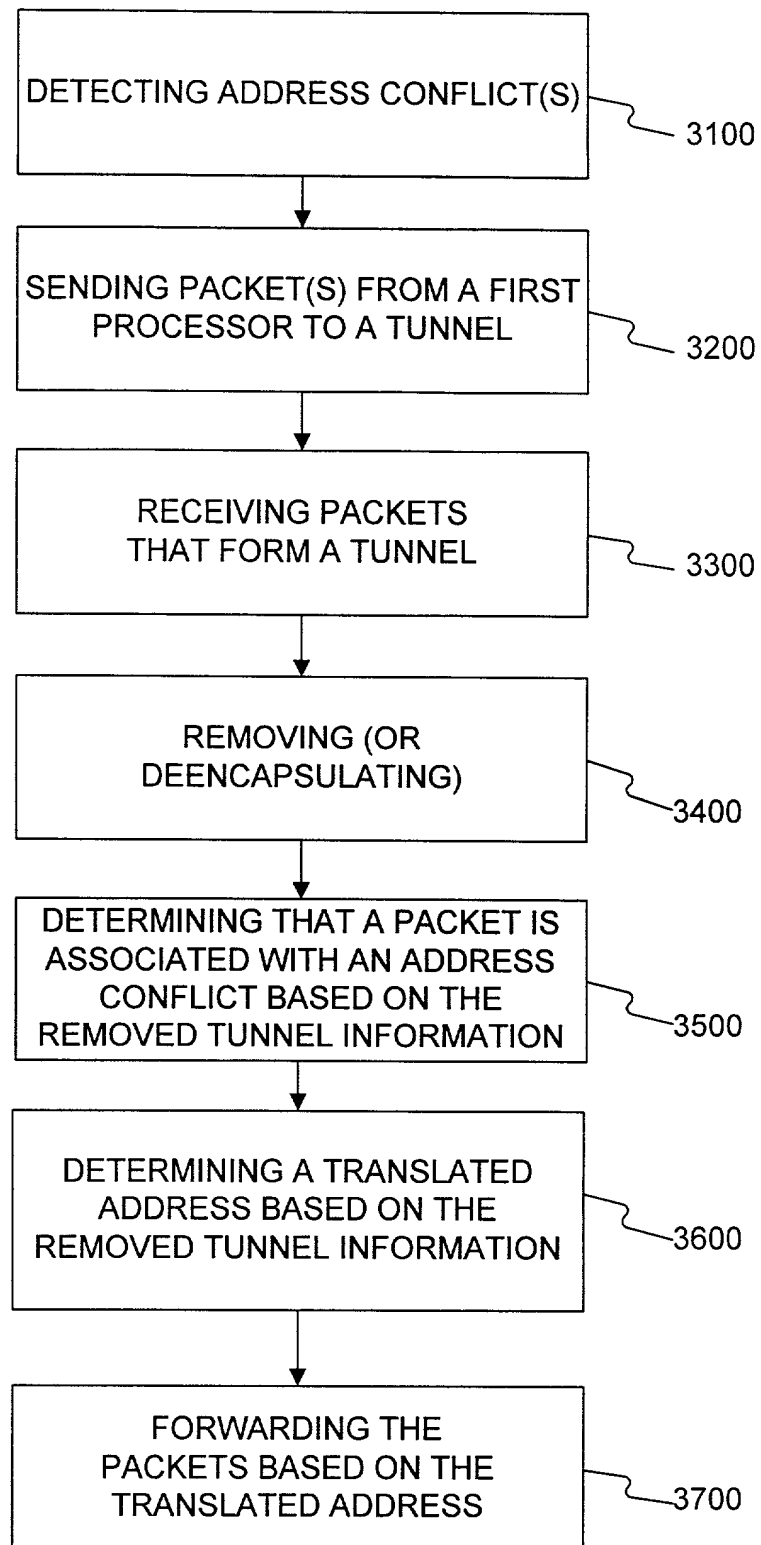


FIG. 1



**FIG. 2**

3000



**FIG. 3**

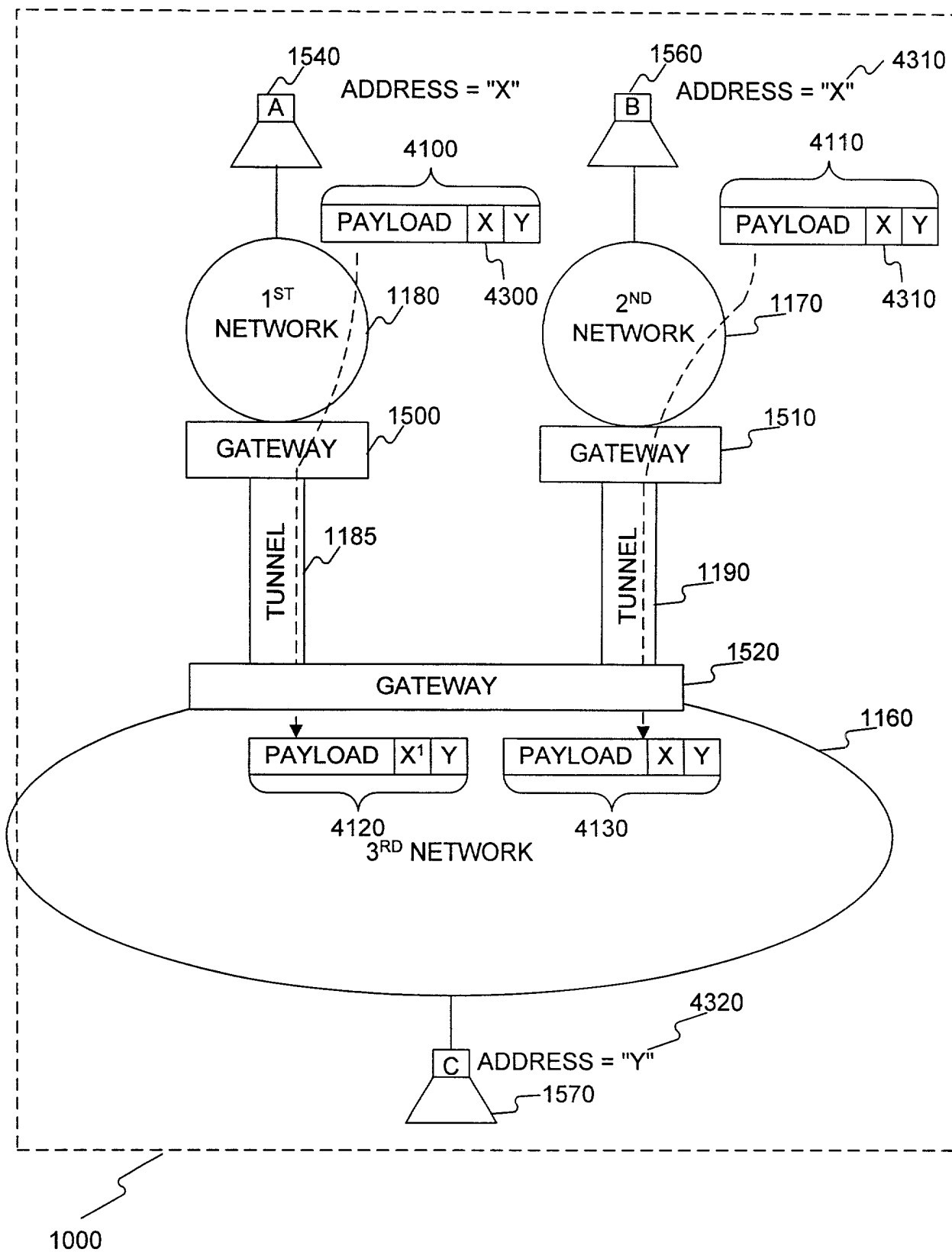


FIG. 4

GATEWAY C

PROCESSOR NAME	LOCAL ADDRESS	TUNNEL INFO	TRANSLATED ADDRESS
A	"X"	"10"	"X"
B	"X"	"11"	"X1"

5000

FIG. 5

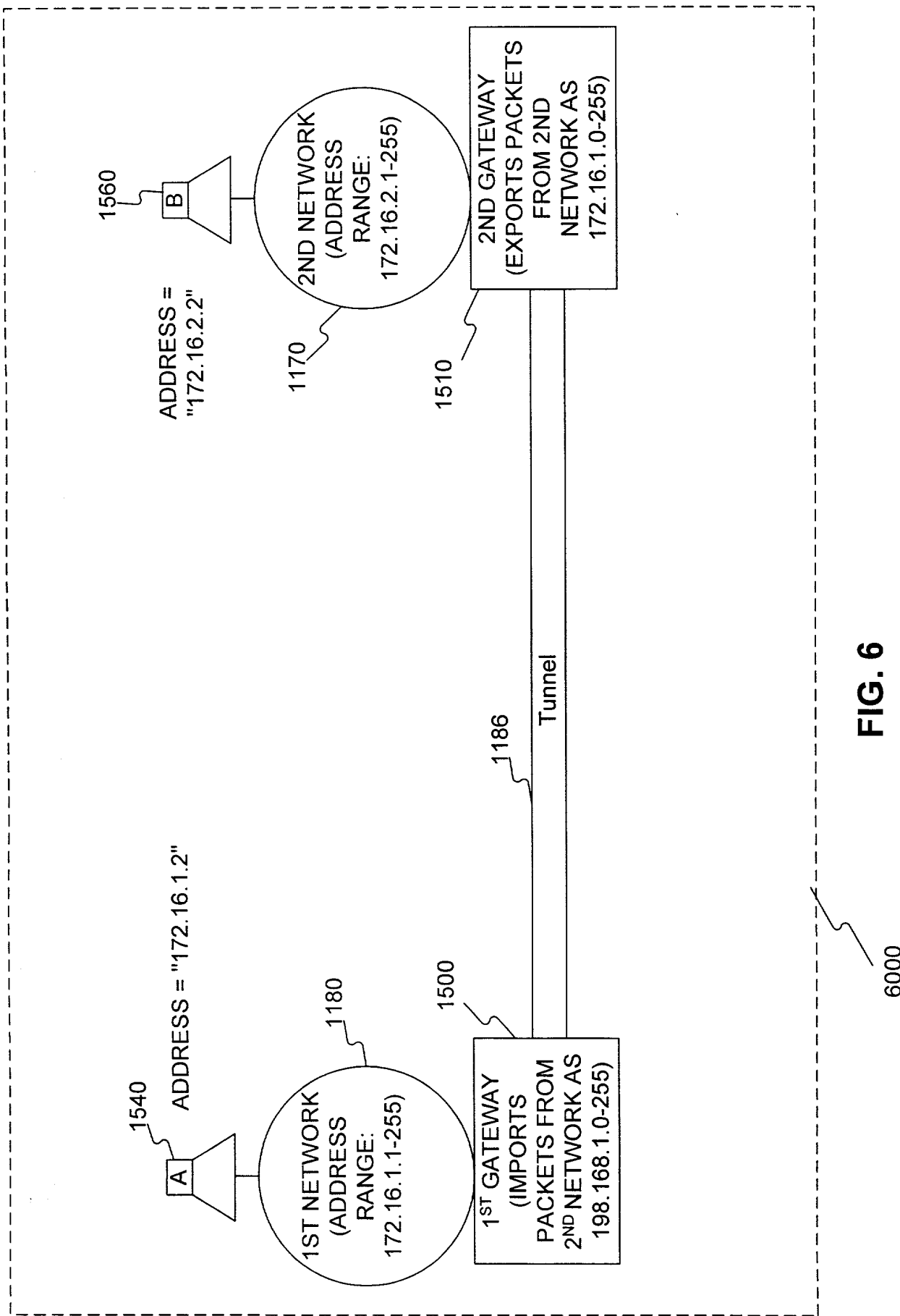


FIG. 6